



CDMA - DSSSTRAINER

MODEL - CDMA100L

This trainer has been designed with a view to provide practical / experimental knowledge of Code Division Multiple Access (CDMA) Modulation/Demodulation technique as practically implemented in mobile communication on a single PCB.



SPECIFICATIONS

1. Power supply requirement : 230V AC, 50 Hz.
2. On Board Pseudo-Noise (PN) Code generator.
Bits : 16 bits
Clock Frequency : 100 KHz
3. On Board Digital Data generator to generate any binary input word of
Word Length : 8 Bits.
Bit Clock Frequency : 6.666 KHz
Data Format : NRZ.
4. Spreading Code Mixer and Unipolar to Bipolar Converter section.
5. Modulator Type : Coherent Binary PSK Modulator
6. Demodulator Type : Coherent Detector.
Low pass Filter cutoff Frequency : 105 KHz
Code Dispersing : Using EX-NOR gate.
7. **Books for Mobile Communication : 10 Nos in pdf Format**
8. **Mp4 Video Class for Mobile Communication : 40 Classes in Mp4 on Pen Drive**
9. Standard Accessories : 1. A Training Manual.
2. Connecting Patch cords.

EXPERIMENTS

1. To study theory of CDMA-DSSS Modulation & Demodulation.
2. To generate CDMA-DSSS signal
3. To demodulate CDMA-DSSS signal using balanced modulators.

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Dealer:-